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碩士論文Institute of Mathematical College of Science

National Taiwan University
Master Thesis

LWR，MPLWE 和 MPLWR 上的全同態加密

# Fully Homomorphic Encryption on LWR，MPLWE and MPLWR 

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中華民國109年7月

July， 2020

## Acknowledgements

經過 2 年的時光，終於完成了這篇論文，我要感謝指導教授陳君明老師带給我的啟發，以及幫我處理一些困難的部分，此外我也很感謝我的同學以及曾經教過我的老師給我一個環境能多寫出這篇論文，非常的感謝大家。

## 摘要

我們改良了 Zvika Brakerski 研發的全同態加密系統，改成使用難題假設 LWR 以及 RLWR 而不是原先使用的 LWE 以及 RLWE 難題假設。並且我們用類似的方法使得可以在 MPLWR 難題假設上使用同態加密。在過去，Rosca 證明了難題假設 MPLWE 的安全性，我們同様使用相似於的方法做成全同態加密。

關鍵字：LWR 同態加密，環 LWR 同態加密，中間積 LWE 同態加密，中間積 LWR 同態加密

## Abstract

We modified the fully homomorphic encryption (FHE) scheme produced by Zvika Brakerski with the hardness assumption learning with rounding (LWR) and ring learning with rounded (RLWR) instead of the hardness assumption learning with error (LWE) and ring learning with rounding (RLWE). And we use the similar methods on the hardness assumption middle product learning with rounding (MPLWR), i.e. making it into FHE. In present, Rosca proves the hardness assumption middle product learning with error (MPLWE). We also use "similar" Brakerski ideas to make it into FHE.

Keywords: Learning with rounding FHE, Ring learning with rounding FHE, Middle product learning with error FHE, Middle product learning with rounding FHE

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## Denotation

| FHE | 全同態加密（Fully Homomorphic Encryption） |
| :--- | :--- |
| MPC | 多方運算（Mutiparty Computation） |
| （R）LWE | （環）從誤差中學習（（Ring）Learning with Error） <br> （R）LWR |
| （環）從四捨五入學習（（Ring）Learning with rounding） |  |
| MPLWE | 中間積從誤差中學習（Middle Product Learning with Error） |
| MPLWR | 中間積從四捨五入中學習（Middle Product Learning with Round－ <br> ing） |

## Chapter 1 Introduction

Fully Homomorphic Encryption (FHE) is a scheme which supports both additions and multiplications on ciphertexts. We can use these properties in cloud computing and multiparty computation (MPC) [2]. In 2009, Craig Gentry, using lattice-based cryptography, described the first homomorphic encryption scheme[4]. It starts from somewhat homomorphic encryption scheme and using Gentry's ideas to make it bootstrappable , i.e., capable of evaluating its own decryption circuit and then at least one more operation. These schemes are so-called first-generation FHE, i.e., bootstraping the somowhat homomorphic encryption scheme.

In 2011, Zvika Brakerski, Craig Gentry and Vinod Vaikuntanathan publish "Fully Homomorphic Encryption without Bootstrapping[11]", representative of second-generation FHE. The second-generation FHE all feature a much slower growth of the noise during the homomorphic encryption. And they are efficient enough for many applications, even without invoking bootstrapping. The security of most of these schemes (second-generation FHE) based on the hardness of the LWE (RLWE)[10] problem, however, the variant LWR (RLWR)[7] does not describe in second-generation. So, we plan to change the scheme on the hardness assumption LWR (RLWR).

### 1.1 Our result

The LWE version has the form $b=\mathbf{A}^{\prime} \mathbf{s}^{\prime}+2 e$ where $b, \mathbf{A}^{\prime}$ are known and $s^{\prime}$ is the secret key and $e \in\{0,1\}$ is the error term. We have to random $e$ to make it secret. We think the error term can neglect and can decide by $\mathbf{A}, \mathbf{s}$ with LWR based, which is often the advantages of LWR. The LWR based encryption have the basic form $b=\left\lceil\frac{1}{2} \mathbf{A s}\right\rfloor$. We think the key point to turn encryption into fully homomorphic encryption is double the error term (error term is $\left\lceil\frac{1}{2} \mathbf{A s}\right\rfloor-\frac{1}{2} \mathbf{A s}$ ). It will finally be modular in the decryption process.

In chapter 3, we also described the MPLWE and MPLWR, which publishes in 2017 and 2019. We use our idea to make MPLWR into FHE and use "similar" Brakerski ideas to make MPLWE into FHE. It means that we introduce a new inner product compatible to MPLWE and MPLWR and use Brakerski ideas to make them into FHE.

### 1.2 Modular Switching

We use the methods described in [11] and change they compatible to our new inner product $\langle\cdot, \cdot\rangle^{\odot}$ since the product used in MPLWE and MPLWR are not standard matrix product, they introduced the "middle product" instead. Our work in modular switching step is to make sure the method described in [11] still work this means that we can also use same methods to make them into FHE. To make our proof clear, the definition of inner product we define will become complex, but we just want to prove the properties written in [11].

### 1.3 FHE Scheme

Since the work in key switching, it looks the same for our LWR (RLWR), MPLWE, MPLWR scheme. Actually, LWR (RLWR) scheme is the same as [11], since it is an equivalent modification from original LWE edition while MPLWE and MPLWR scheme should look different, i.e. we should read section 3.4 to know what should do in MPLWE and MPLWR .

### 1.4 Compare to LWE (RLWE)

We can use the hardness assumption LWR (RLWR), MPLWE and MPLWR which usually think to be harder than LWE hardness assumption. In [8], it proves that the worst cases of LWR (RLWR) is harder than LWE (RLWE) average cases. And in [9], it proves that MPLWE is harder than polynomial LWE problems, also in [10], it proves that MPLWR is harder than polynomial LWR problems. And for LWR and MPLWR based encryption, we expand the dimension of public key size to trade-off the error generation.

## Chapter 2 Preliminaries

## Definition 1. (LWE)

For security parameter $\lambda$, let $n=n(\lambda)$ be an integer dimension, let $q=q(\lambda) \geq 2$ be an integer, and let $\chi=\chi(\lambda)$ be a distribution over $\mathbb{Z}$. The $\operatorname{LWE}_{n, q, \chi}$ problem is to distinguish the following two distributions: In the first distribution, one samples $\left(a_{i}, b_{i}\right)$ uniformly from $\mathbb{Z}_{q}^{n+1}$. In the second distribution, one first draws $s \leftarrow \mathbb{Z}_{q}^{n}$ uniformly and then samples $\left(a_{i}, b_{i}\right) \in \mathbb{Z}_{q}^{n+1}$ by sampling $a_{i} \leftarrow \mathbb{Z}_{q}^{n}$ uniformly, $e_{i} \leftarrow \chi$, and setting $b_{i}=\left\langle a, a \cdot s_{i}+e\right\rangle$. The $\operatorname{LWE}_{n, q, \chi}$ assumption is that the $\operatorname{LWE}_{n, q, \chi}$ problem is infeasible.

## Definition 2. (LWR)

For security parameter $\lambda$, let $n=n(\lambda)$ be an integer dimension, let $q=q(\lambda) \geq 2$ be an integer and $p=p(\lambda)<q$, and let $\chi=\chi(\lambda)$ be a distribution over $\mathbb{Z}$. The $\operatorname{LWR}_{n, q, \chi}$ problem is to distinguish the following two distributions: In the first distribution, one samples ( $a_{i}, b_{i}$ ) uniformly from $\mathbb{Z}_{q}^{n+1}$. In the second distribution, one first draws $s \leftarrow \mathbb{Z}_{q}^{n}$ uniformly and then samples $\left(a_{i}, b_{i}\right) \in \mathbb{Z}_{q}^{n+1}$ by sampling $a_{i} \leftarrow \mathbb{Z}_{q}^{n}$ uniformly and setting $b_{i}=\left\lceil\frac{p}{q} \cdot a \cdot s_{i}\right\rfloor$. The $\operatorname{LWR}_{n, q, \chi}$ assumption is that the $\operatorname{LWR}_{n, q, \chi}$ problem is infeasible.

Definition 3. Let $d_{a}, d_{b}, d, k$ be integers such that $d_{a}+d_{b}-1=d+2 k$. The middle-product $\odot_{d}: R_{a}^{<d}[x] \times R_{b}^{<d}[x] \rightarrow R^{<d}[x]$ is the map:

$$
(a, b) \mapsto a \odot_{d} b=\left\lfloor\frac{(a \cdot b) \bmod x^{k+d}}{x^{k}}\right\rfloor
$$

We use the same notation $\odot_{d}$ for every $d_{a}, d_{b}$ such that $d_{a}+d_{b}-1-d$ is non-negative and even.

Definition 4. (modular rounding function)
Let p and q be integers both larger than 1 . A modular rounding function $\lfloor\cdot\rceil_{p}: \mathbb{Z}_{q} \rightarrow$ $\mathbb{Z}_{p}$ as $\lfloor x\rceil_{p}=\left\lfloor\frac{p}{q} \cdot x\right\rceil \bmod p$. The rounding function extends component-wise to vectors over $\mathbb{Z}_{q}$ and coefficient-wise to polynomials in $\mathbb{Z}_{q}[x]$. Note that we use the same notation as Banerjee et al. [1] for the purpose of coherence. It is also possible to use the floor rounding function $b \cdot c$, where each element is rounded down to the next smaller integer, as for instance done by Chen et al. [8].

Definition 5. (MP distribution)
Let $n, d>0, q \geq 2$, and $\chi$ a distribution over $\mathbb{R}^{<d}[x]$. For $s \in \mathbb{Z}_{q}^{<n+d-1}[x]$, we define the distribution $\mathrm{MP}_{\mathrm{q}, \mathrm{n}, \mathrm{d}, \chi}(\mathrm{s})$ over $\mathbb{Z}_{q}^{<n}[x] \times \mathbb{R}_{q}^{<d}[x]$ as the one obtained by: sampling $a \leftarrow U\left(\mathbb{Z}_{q}^{<n}[x]\right), e \leftarrow \chi$ and returning $\left(a, b=a \odot_{d} s+e\right)$.

## Definition 6. (MP-LWE)

Let $n, d>0, q \geq 2$, and a distribution $\chi$ over $\mathbb{R}^{<d}[x]$. The (decision) MP-LWE ${ }_{n, d, q, \chi}$ consists in distinguishing between arbitrarily many samples from $\mathrm{MP}_{q, n, d, \chi}(s)$ and the same number of samples from $U\left(\mathbb{Z}_{q}^{<n}[x] \times \mathbb{R}_{q}^{<d}[x]\right)$, with non-negligible probability over the choices of $s \leftarrow U\left(\mathbb{Z}_{q}^{<n+d-1}[x]\right)$.

Definition 7. (MP-CLWR assumption)
Let $d, n, p, q$ and $t$ be positive integers fulfilling $0<d \leq n$ and $q \geq p \geq 2$. Choose
$s$ uniformly at random over $\left(\mathbb{Z}_{q}^{<n+d-1}[x]\right)^{\times}$. Denote by $\chi_{s}$ the distribution of $\left(a,\left[a \odot_{d}\right.\right.$ $s\rceil_{p}$ ), where $a \leftarrow U\left(\mathbb{Z}_{q}^{<n}[x]\right)$, and denote by $\mathcal{U}$ the distribution of $\left(a,\lfloor b\rceil_{p}\right)$, where $a \leftarrow$. $U\left(\mathbb{Z}_{q}^{<n}[x]\right)$ and $b \leftarrow U\left(\mathbb{Z}_{q}^{<d}[x]\right)$. For $i \in\{1,2\}$ define the input for $S_{i}$ as (var, con ), where $v a r_{1}$ denotes the distribution $\chi_{s}^{t}$, and var $_{2}$ the distribution $\mathcal{U}^{t}$, and con is an arbitrary distribution over $\{0,1\}^{*}$ which is independent from var $_{1}$ and var $_{2}$. For a fixed challenger $\mathcal{C}$ let $\mathcal{P}_{\mathcal{C}, \mathcal{A}}$ be the probability for an adversary $\mathcal{A}$ to win $\operatorname{Exp}_{1}\left(\mathcal{C}, \mathcal{A}, \mathcal{S}_{1}\right)$, while $\mathcal{Q}_{\mathcal{C}, \mathcal{A}}$ be that for $\mathcal{A}$ to win $\operatorname{Exp}_{2}\left(\mathcal{C}, \mathcal{A}, \mathcal{S}_{2}\right)$.

Definition 8. Let $l, r_{a}, r_{b}$ be the integers. The middle inner product $\langle a, b\rangle_{\left(l, r_{a}, r_{b}\right)}^{\odot_{d}}: R_{a}^{l+<d}[x] \times$ $R_{b}^{l+<d}[x] \rightarrow R^{l+<d}[x]$ is the function such that $a, b$ do standard inner product for first $l$ entries of $a, b$, and the remainders do middle product for $r_{a}$ each rows of remainder $a$ products $r_{b}$ each rows of remainder $b$.

(The first $l$ entries compute stardard inner product and plus $r_{a 1} \odot_{d} r_{b 2}+\cdots+r_{a k} \odot r_{b k}$, where $l=k \times h$ )

Notice that we use the notation $\langle a, b\rangle{ }^{\odot_{d}}$ to denote $\langle a, b\rangle_{(d, t,<n+d+k-1)}^{\odot_{d}}$

## Chapter 3 Our Construction

We describe each basic encryption scheme on section 3.1 to section 3.3 and described the key switching for each scheme in section 3.4 and section 3.5. In section 3.6 we lay out our FHE scheme for each encryption scheme.

### 3.1 Basic LWR (RLWR) encryption scheme

In this section, our encryption scheme do not need to generate the error terms and we use the hardness LWR (RLWR) instead. Basic encryption scheme:

- E.Setup: Choose $d=d(\lambda, \mu, b), n=n(\lambda, \mu, b), \chi=\chi(\lambda, \mu, b) N=\lceil(2 n+$ 3) $\log q\rfloor$ same as the $[11]$ scheme. And let $R=\mathbb{Z}[x] /\left(x^{d}+1\right)$
- E.SecretKeyGen: $s^{\prime} \leftarrow \chi^{n}$. $s k=\mathbf{s}=\left(1, s^{\prime}[1], s^{\prime}[2], \ldots, s^{\prime}[n]\right) . \mathbf{s} \in R_{q}^{n+1}$
- E.PublicKeyGen: Generate matrix $\mathbf{A}^{\prime} \in R_{q}^{N \times n}$ and set $\mathbf{b}=2^{2} \times\left\lceil\frac{1}{2} \mathbf{A}^{\prime} s^{\prime}\right\rfloor-(2-$ 1) $\mathbf{A}^{\prime} s^{\prime} \in R_{q}$. Set $\mathbf{A}=\left[\mathbf{b} \mid-\mathbf{A}^{\prime}\right]$, notice that $\langle\mathbf{A}, \mathbf{s}\rangle=2^{2}\left(\left\lceil\frac{1}{2} \mathbf{A}^{\prime} s^{\prime}\right\rfloor-\frac{1}{2} \mathbf{A}^{\prime} s^{\prime}\right)$
- E.Encryption: To encryption a message $m \in R_{2}$. Set $\mathbf{m}=(m, 0, \ldots, 0) \in R_{2}^{n+1}$, random $\mathbf{r} \in R_{2}^{N}$ and output the cipertext $\mathbf{c}=\mathbf{m}+\mathbf{A}^{T} \mathbf{r}$
- E.Decryption: Output $m=\langle\mathbf{c}, \mathbf{s}\rangle_{q} \bmod 2$


### 3.2 Basic MPLWE encryption scheme

In this section, we adjust the encryption scheme describe in [9] and compatible to our homomorphic encryption scheme. Let $\chi=\left\lfloor D_{\alpha q}\right\rceil$ denote the distribution over $\mathbb{Z}^{<d+k}[x]$ where each coefficient is sampled from $D_{\alpha \cdot q}$ and then rounded to nearest integer. And let $t \geq 2$.

- E.KeyGen: Random $s^{\prime} \leftarrow U\left(\mathbb{Z}_{q}^{<n+d+k-1}[x]\right)$. For every $i \leq t$, random $a_{i}^{\prime} \leftarrow$ $U\left(\mathbb{Z}_{q}^{<n}[x]\right), e_{i} \leftarrow \chi$ and compute $b_{i}=a_{i}^{\prime} \odot_{d+k} s^{\prime}+2 e_{i} \in \mathbb{Z}_{q}^{<d+k}[x]$. The secret key sk=s $=\left(1, s^{\prime}\right) \in \mathbb{Z}_{q}^{<n+2 d+k-1}[x]$ and the public key $p k=\left(a_{i}^{\prime}, b_{i}\right)_{i \leq t}$. Set $\mathbf{A}=\left[\mathbf{b} \mid-\mathbf{A}^{\prime}\right]$
- E.Encryption: To encryption a message $m \in\{0,1\}^{<d}[x]$. Set $\mathbf{m}=(m, 0, \ldots, 0) \in$ $\mathbb{Z}^{<d+t}[x]$, random $\mathbf{r}_{\mathbf{i}} \leftarrow U\left(\{0,1\}^{k+1}[x]\right)$ and output the ciphertext $\mathbf{c}=\mathbf{m}+A^{T} \overline{\odot_{d}} \mathbf{r}$
- E.Decryption: Output $m=\langle\mathbf{c}, \mathbf{s}\rangle_{q}^{\odot_{d}} \bmod 2$


### 3.3 Basic MPLWR encryption scheme

In this section, we lay out our encryption scheme of MPLWR with the hardness assumption describe in [10]. Let $\chi=\left\lfloor D_{\alpha q}\right\rceil$ denote the distribution over $\mathbb{Z}^{<d+k}[x]$ where each coefficient is sampled from $D_{\alpha \cdot q}$ and then rounded to nearest integer. And let $t \geq 3$.

- E.KeyGen: Random $s^{\prime} \leftarrow U\left(\mathbb{Z}_{q}^{<n+d+k-1}[x]\right)$. For every $i \leq t$, random $a_{i}^{\prime} \leftarrow$ $U\left(\mathbb{Z}_{q}^{<n}[x]\right), e_{i} \leftarrow \chi$ and compute $b_{i}=4\left\lceil a_{i}^{\prime} \odot_{d+k} s^{\prime}\right\rfloor_{2}-a_{i}^{\prime} \odot_{d+k} s^{\prime} \in \mathbb{Z}_{q}^{d+k}[x]$. The secret key $s k=\mathbf{s}=\left(1, s^{\prime}\right) \in \mathbb{Z}_{q}^{<n+2 d+k-1}[x]$ and the public key $p k=\left(a_{i}^{\prime}, b_{i}\right)_{i \leq t}$. Set $\mathbf{A}=\left[\mathbf{b} \mid-\mathbf{A}^{\prime}\right]$
- E.Encryption: To encryption a message $m \in\{0,1\}^{<d}[x]$. Set $\mathbf{m}=(m, 0, \ldots, 0) \in$ $\mathbb{Z}^{<d+t}[x]$, random $\mathbf{r}_{\mathbf{i}} \leftarrow U\left(\{0,1\}^{k+1}[x]\right)$ and output the ciphertext $\mathbf{c}=\mathbf{m} \mp A^{T} \overline{\odot_{d}} \mathbf{r}$
- E.Decryption: Output $m=\langle\mathbf{c}, \mathbf{s}\rangle_{q}^{\odot_{d}} \bmod 2$


### 3.4 Key Switching for MPLWE and MPLWR based

FHE is a scheme which supports both additions and multiplications on ciphertexts. It is obviously that the addition property is barely not support, so we focus on how to satisfy multiplication property. Kroncker product is a heuristic way in our scheme. That is, to compute $m_{1} \times m_{2}$, we compute $\left\langle\mathbf{c}_{1} \otimes \mathbf{c}_{2}, \mathbf{s} \otimes \mathbf{s}\right\rangle$. However, if the levels of the multiplication are too depth, the noise will become huge. In [11], its solution is the switch key method, which refreshes the ciphertexts to fix long. The scheme is evidently compatible to our LWE (RLWE) scheme. And our work is to prove that it can be compatible to MPLWE and MPLWR scheme. We use the notation denote in [11].

- BitDecomp $\left(\mathbf{x} \in \mathbb{Z}_{q}^{n}[x], q\right)$ decomposes $\mathbf{x}$ into its bit representation for each entries. Namely, write $\mathbf{x}=\sum_{j=0}^{[\log q]} 2^{j} \cdot \mathbf{u}_{j}$, where all of the $\mathbf{u}_{j} \in\{0,1\}^{n}[x]$, and output $\left(\mathbf{u}_{0}, \ldots, \mathbf{u}_{[\log q\rfloor}\right) \in \mathbb{Z}_{2}^{[\log q\rfloor \times n}[x]$
- Powerof2 $\left(\mathbf{x} \in \mathbb{Z}_{q}^{m}[x], q\right)$ Output $\left(\mathbf{x}, 2 \cdot \mathbf{x}, \ldots, 2^{\lceil\log q\rfloor} \cdot \mathbf{x}\right) \in \mathbb{Z}_{q}^{[\log q\rfloor \times m}[x]$

Lemma 1. We have $\langle\operatorname{BitDecomp}(\mathbf{c}, \mathbf{q}) \text {, Powerof } 2(\mathbf{s}, \mathbf{q})\rangle_{([\log \mathrm{q} \mid, \mathrm{t}, \mathrm{n}+\mathrm{d}+\mathrm{k}-1)}^{\odot_{d}}=\langle\mathbf{c}, \mathbf{s}\rangle^{\odot_{d}} \bmod q$ for vectors $\mathbf{c} \in \mathbb{Z}_{q}^{d+t}, \mathbf{s} \in \mathbb{Z}_{q}^{n+2 d+k-1}$

Proof. By lemma, we know that we can add first and then compute the middle product, hence if we see the $\operatorname{BitDecomp}(\mathbf{c}, q)$ and $\operatorname{Powerof} 2(\mathbf{s}, q)$ as extending $\mathbf{c}, \mathbf{s}$ into bit compu-
tations then it is correct. Hence

$$
\begin{aligned}
\langle\mathbf{c}, \mathbf{s}\rangle^{\odot_{d}} & =\left\langle\sum_{j=0}^{\lceil\log q\rfloor} 2^{j} \cdot \mathbf{u}_{j}, \mathbf{s}\right\rangle^{\odot_{d}} \\
& =\sum_{j=0}^{\lceil\log q\rfloor}\left\langle 2^{j} \cdot \mathbf{u}_{j}, \mathbf{s}\right\rangle^{\odot_{d}} \\
& =\sum_{j=0}^{\lceil\log q\rfloor}\left\langle\mathbf{u}_{j}, 2^{j} \cdot \mathbf{s}\right\rangle^{\odot_{d}} \\
& =\langle\operatorname{BitDecomp}(\mathbf{c}, \mathbf{q}), \operatorname{Powerof} 2(\mathbf{s}, \mathbf{q})\rangle_{(\mathrm{d}[\log \mathrm{q}], \mathrm{t}, \mathrm{n}+\mathrm{d}+\mathrm{k}-\mathrm{l})}^{\odot_{\mathrm{d}}}
\end{aligned}
$$

By the proof of our main lemma we can now introduce how to refresh the ciphertexts into fixed long.

SwitchKeyGen $\left(s_{1} \in \mathbb{Z}_{q}^{n_{1}}[x], s_{2} \in \mathbb{Z}_{q}^{n_{2}}[x]\right)$ :

1. Run $\mathbf{A} \leftarrow$ E.PublicKeyGen $\left(s_{2}, N\right)$ for $\mathbf{N}=n_{1} \cdot\lceil\log q\rfloor$.
2. Set $\mathbf{B} \leftarrow \mathbf{A}+$ Powerof2( $\mathbf{s}_{1}$ ) (add it to A's first column). Output $\tau_{s_{1} \rightarrow s_{2}}=\mathbf{B}$
$\operatorname{SwitchKey}\left(\tau_{s_{1} \rightarrow s_{2}}, c_{1}\right)$ : Output $c_{2}=\operatorname{BitDecomp}\left(\mathbf{c}_{1}\right)^{\mathrm{T}} \overline{{ }_{\mathrm{d}}} \mathbf{B} \in \mathbb{Z}_{\mathrm{q}}^{\mathrm{n}_{2}}[\mathbf{x}]$

Lemma 2. Let $\mathbf{s}_{\mathbf{1}}, \mathbf{s}_{\mathbf{2}}, q, n_{1}, n_{2}, \mathbf{A}, \mathbf{B}=\tau_{\mathbf{s}_{1} \rightarrow \mathbf{s}_{2}}$ be as in SwitchKeyGen $\left(\mathbf{s}_{\mathbf{1}}, \mathbf{s}_{2}\right)$, and let $\mathbf{A} \odot_{d} \mathbf{s}_{2}=2 \mathbf{e}_{2} \in \mathbb{Z}_{q}^{n_{2}}[x]$ (For MPLWR we have $\left.\mathbf{A} \overline{\odot_{d}} \mathbf{s}_{2}=2\left(2\left\lceil\mathrm{a}_{\mathrm{i}}^{\prime} \odot_{\mathrm{d}} \mathrm{s}_{\mathrm{i}}^{\prime}\right]_{2}-\mathrm{a}_{\mathrm{i}}^{\prime} \odot_{\mathrm{d}} \mathrm{s}_{\mathrm{i}}^{\prime}\right)\right)$. Let $\mathbf{c}_{1} \in \mathbb{Z}_{q}^{n_{1}}[x]$ and $\mathbf{c}_{2} \leftarrow \operatorname{SwitchKey}\left(\tau_{s_{1} \rightarrow \mathrm{~s}_{2}}, \mathbf{c}_{1}\right)$. Then,

$$
\left\langle\mathbf{c}_{2}, \mathbf{s}_{2}\right\rangle^{\odot_{d}}=2\left\langle\operatorname{BitDecomp}\left(\mathbf{c}_{1}\right), \mathbf{e}_{2}\right\rangle_{\left([\log \mathrm{q}], \mathrm{n}_{1}-\mathrm{d}, \mathrm{n}_{2}-\mathrm{d}\right)}^{\odot_{\mathrm{d}}}+\left\langle\mathbf{c}_{1}, \mathbf{s}_{1}\right\rangle^{\odot_{\mathrm{d}}}
$$

Proof.

$$
\begin{aligned}
\left\langle\mathbf{c}_{2}, \mathbf{s}_{2}\right\rangle & =\operatorname{BitDecomp}\left(\mathbf{c}_{1}\right) \odot_{\mathrm{d}} \mathbf{B} \widetilde{\odot}_{\mathrm{d}} \mathbf{s}_{1} \\
& =\operatorname{BitDecomp}\left(\mathbf{c}_{1}\right) \odot_{\mathrm{d}}\left(2 \mathbf{e}_{2}+\operatorname{Powerof} 2\left(\mathbf{s}_{1}\right)\right) \\
& =2\left\langle\operatorname{BitDecomp}\left(\mathbf{c}_{1}\right), \mathbf{e}_{2}\right\rangle_{\left([\log \mathrm{q}], \mathrm{n}_{1}-\mathrm{d}, \mathrm{n}_{2}-\mathrm{d}\right)}^{\odot_{\mathrm{d}}}+\left\langle\operatorname{BitDecomp}\left(\mathbf{c}_{1}\right), \operatorname{Powerof} 2\left(\mathbf{s}_{1}\right)\right\rangle_{\left([\log \mathrm{q}], \mathrm{n}_{1}-\mathrm{d}, \mathrm{n}_{2}-\mathrm{d}\right)}^{\odot_{\mathrm{d}}} \\
& =2\left\langle\operatorname{BitDecomp}\left(\mathbf{c}_{1}\right), \mathbf{e}_{2}\right\rangle_{\left([\log \mathrm{q}], \mathrm{n}_{1}-\mathrm{d}, \mathrm{n}_{2}-\mathrm{d}\right)}^{\odot_{\mathrm{d}}}+\left\langle\mathbf{c}_{1}, \mathbf{s}_{1}\right\rangle \odot_{\mathrm{d}}
\end{aligned}
$$

### 3.5 Key Switching for LWR(RLWR) based

In this section we will provide the LWR(RLWR) based key switching. Since it is an equivalent modification from LWE(RLWE) based, we will find it similar to it. We will use the notation denoted in [11].

- BitDecomp $\left(\mathbf{x} \in R_{q}^{n}, q\right)$ decomposes $\mathbf{x}$ into its bit representation for each entries.

Namely, write $\mathbf{x}=\sum_{j=0}^{[\log q]} 2^{j} \cdot \mathbf{u}_{j}$, where all of the $\mathbf{u}_{j} \in R_{2}^{n}$, and output $\left(\mathbf{u}_{0}, \ldots, \mathbf{u}_{[\log q]}\right) \in$ $R_{2}^{[\log q] \times n}$

- Powerof $2\left(\mathbf{x} \in R_{q}^{m}, q\right)$ Output $\left(\mathbf{x}, 2 \cdot \mathbf{x}, \ldots, 2^{[\log q\rfloor} \cdot \mathbf{x}\right) \in R_{q}^{[\log q] \times m}$

Lemma 3. For vectors $\mathbf{c}, \mathbf{s}$ of equal length, we have $\langle\operatorname{BitDecomp}(\mathbf{c}, \mathbf{q})$, Powersof2 $(\mathbf{s}, \mathbf{q})\rangle$ $=\langle\mathbf{c}, \mathbf{s}\rangle \bmod q$.

Proof.

$$
\begin{aligned}
\langle\mathbf{c}, \mathbf{s}\rangle & =\left\langle\sum_{j=0}^{\lceil\log q\rceil} 2^{j} \cdot \mathbf{u}_{j}, \mathbf{s}\right\rangle \\
& =\sum_{j=0}^{\lceil\log q\rceil}\left\langle 2^{j} \cdot \mathbf{u}_{j}, \mathbf{s}\right\rangle \\
& =\sum_{j=0}^{\lceil\log q\rceil}\left\langle\mathbf{u}_{j}, 2^{j} \cdot \mathbf{s}\right\rangle \\
& =\langle\mathbf{c}, \mathbf{s}\rangle\langle\operatorname{BitDecomp}(\mathbf{c}, \mathbf{q}), \operatorname{Powerof} 2(\mathbf{s}, \mathbf{q})\rangle
\end{aligned}
$$

And hence we have
SwitchKeyGen $\left(s_{1} \in R_{q}^{n_{1}}, s_{2} \in R_{q}^{n_{2}}\right)$ :

1. Run $\mathbf{A} \leftarrow$ E.PublicKeyGen $\left(s_{2}, N\right)$ for $\mathbf{N}=n_{1} \cdot\lceil\log q\rfloor$.
2. Set $\mathbf{B} \leftarrow \mathbf{A}+$ Powerof2( $\mathbf{s}_{1}$ ) (add it to $\mathbf{A}$ 's first column). Output $\tau_{s_{1} \rightarrow s_{2}}=\mathbf{B}$
$\operatorname{SwitchKey}\left(\tau_{s_{1} \rightarrow s_{2}}, c_{1}\right)$ : Output $c_{2}=\operatorname{BitDecomp}\left(\mathbf{c}_{1}\right)^{\mathrm{T}} \cdot \mathbf{B} \in \mathrm{R}_{\mathrm{q}}^{\mathrm{n}_{2}}$
Lemma 4. Let $\mathbf{s}_{\mathbf{1}}, \mathbf{s}_{\mathbf{2}}, q, n_{1}, n_{2}, \mathbf{A}, \mathbf{B}=\tau_{\mathbf{s}_{1} \rightarrow \mathbf{s}_{2}}$ be as in SwitchKeyGen( $\left.\mathbf{s}_{\mathbf{1}}, \mathbf{s}_{2}\right)$, and let $\mathbf{A}$. $\mathbf{s}_{2}=2^{2}\left(\left\lceil\frac{1}{2} \mathbf{A}^{\prime} s^{\prime}\right\rfloor-\frac{1}{2} \mathbf{A}^{\prime} s^{\prime}\right) \in R_{q}^{N}$. Let $\mathbf{c}_{1} \in R_{q}^{n_{1}}$ and $\mathbf{c}_{2} \leftarrow \operatorname{SwitchKey}\left(\tau_{s_{1} \leftarrow s_{2}}, \mathbf{c}_{1}\right)$. Then,

$$
\left\langle\mathbf{c}_{2}, \mathbf{s}_{2}\right\rangle=2\left\langle\operatorname{BitDecomp}\left(\mathbf{c}_{1}\right), 2\left(\left\lceil\frac{1}{2} \mathbf{A}^{\prime} \mathbf{s}^{\prime}\right\rfloor-\frac{1}{2} \mathbf{A}^{\prime} \mathbf{s}^{\prime}\right)\right\rangle+\left\langle\mathbf{c}_{1}, \mathbf{s}_{1}\right\rangle \quad \bmod \mathrm{q}
$$

Proof.

$$
\begin{aligned}
\left\langle\mathbf{c}_{2}, \mathbf{s}_{2}\right\rangle & =\operatorname{BitDecomp}\left(\mathbf{c}_{1}\right)^{\mathrm{T}} \cdot \mathbf{B} \cdot \mathbf{s}_{2} \\
& =\operatorname{BitDecomp}\left(\mathbf{c}_{1}\right)^{\mathrm{T}} \cdot\left(2^{2}\left(\left\lceil\frac{1}{2} \mathbf{A}^{\prime} \mathbf{s}^{\prime}\right\rfloor-\frac{1}{2} \mathbf{A}^{\prime} \mathbf{s}^{\prime}\right)+\operatorname{Powerof} 2\left(\mathbf{s}_{1}\right)\right) \\
& =2\left\langle\operatorname{BitDecomp}\left(\mathbf{c}_{1}\right), 2\left(\left\lceil\frac{1}{2} \mathbf{A}^{\prime} \mathbf{s}^{\prime}\right\rfloor-\frac{1}{2} \mathbf{A}^{\prime} \mathbf{s}^{\prime}\right)\right\rangle+\left\langle\operatorname{BitDecomp}\left(\mathbf{c}_{1}\right), \text { Powerof } 2\left(\mathbf{s}_{1}\right)\right\rangle \\
& =2\left\langle\operatorname{BitDecomp}\left(\mathbf{c}_{1}, 2\left(\left\lceil\frac{1}{2} \mathbf{A}^{\prime} \mathbf{s}^{\prime}\right\rfloor-\frac{1}{2} \mathbf{A}^{\prime} \mathbf{s}^{\prime}\right)\right\rangle+\left\langle\mathbf{c}_{1}, \mathbf{s}_{1}\right\rangle\right.
\end{aligned}
$$

### 3.6 FHE scheme

The previous lemma provide us compatible to [11] scheme. In this section, we will present our scheme modify from [11].(Notation: $L_{\mathbf{c}}(\mathbf{x})=\langle\mathbf{c}, \mathbf{x}\rangle^{\odot_{q}}$ is a ciphertextdependent linear equation over the coefficients of $\mathbf{x}$, and a linear equation $L_{\mathbf{c}_{1}, \mathbf{c}_{2}}^{\text {long }}(\mathbf{x} \otimes \mathbf{x})$ is a linear equation over the coefficient over the coefficients of $\mathbf{x} \otimes \mathbf{x}$ )

- FHE.KenGen. For $j=L \rightarrow 0$ do

1. Generation $s_{j}$ and $\mathbf{A}_{j}$ for each encryption scheme.
2. Set $s_{j}^{\prime}=s_{j} \otimes s_{j}$ kronecker tensor in here.
3. $\operatorname{Set} s_{j}^{\prime \prime}=\operatorname{BitDecomp}\left(s_{\mathrm{j}}^{\prime}, \mathbf{q}_{\mathrm{j}}\right)$
4. Run $\tau_{s_{j}^{\prime \prime} \rightarrow s_{j}}=\operatorname{Switch} \operatorname{KeyGen}\left(s_{j}^{\prime \prime}, s_{j-1}\right)$ (Omit this step in the beginning i.e. $j=L$ )

- FHE.Enc. Basic encryption scheme to encrypt messages.
- FHE.Dec. Suppose the cipertext is under key $s_{j}$. Decrypt the message under key $s_{j}$ in E.Decryption
- FHE.Add. Take two cipertexts encrypt under key $s_{j}$ (if not, do FHE.Refresh to make it encrypts under same key $s_{j}$ ). Set $\mathbf{c}_{3}=\mathbf{c}_{1}+\mathbf{c}_{2} \bmod q_{j}$. (In [11], it expands the cipertexts size to make it indistinguishable to FHE.Mult) Hence we interpret $\mathbf{c}_{3}$ under $s_{j}^{\prime}$ and output

$$
\mathbf{c}_{4}=\text { FHE.Refresh }\left(\mathbf{c}_{3}, \tau_{\mathrm{s}_{\mathrm{j}}^{\prime \prime} \rightarrow \mathrm{s}_{\mathrm{j}-1}}, \mathrm{q}_{\mathrm{j}}, \mathrm{q}_{\mathrm{j}-1}\right)
$$

- FHE.Mult. Take two cipertexts encrypt under key $s_{j}$ (if not, do FHE.Refresh to make it encrypts under same key $s_{j}$ ). The new cipertexts is the kronecker tensor of two cipertexts, with key $s_{j}^{\prime}=s_{j} \otimes s_{j}$, and store it into a line, i.e. $\mathbf{c}_{3}=L_{c_{1}, c_{2}}^{\text {long }}(\mathbf{x} \otimes \mathbf{x})$ and output

$$
\mathbf{c}_{4}=\text { FHE.Refresh }\left(\mathbf{c}_{3}, \tau_{\mathrm{s}_{\mathrm{j}}^{\prime \prime} \rightarrow s_{\mathrm{j}-1}}, \mathrm{q}_{\mathrm{j}}, \mathrm{q}_{\mathrm{j}-1}\right)
$$

- FHE.Refresh. Takes a cipertext encrypted under $s_{j}^{\prime}$, the auxiliary information $\tau_{s_{j}^{\prime \prime} \rightarrow s_{j-1}}$ to facilitate key switching, and the current and next modulo $q_{j}$ and $q_{j-1}$. Do the following:

1. Expand: Set $\mathbf{c}_{1}=\operatorname{Powerof} 2\left(\mathbf{c}, q_{j}\right)$.
2. Switch Moduli: $\operatorname{Set} \mathbf{c}_{2}=\operatorname{Scale}\left(\mathbf{c}_{1}, q_{j}, q_{j-1}, 2\right)$, a ciphertext under the key $s_{j}^{\prime \prime}$ for modulus $q_{j-1}$.
3. Switch Keys: Output $\mathbf{c}_{3}=\operatorname{Switch} \operatorname{Key}\left(\tau_{s_{j}^{\prime \prime} \rightarrow s_{j-1}}, \mathbf{c}_{2}, q_{j-1}\right)$, a ciphertext under the key $\mathbf{s}_{j-1}$ for modulus $q_{j-1}$.

Since we have expressed our notation compatible to [11] it may look similar to its scheme. But, it actually does different things in MPLWE and MPLWR schemes. And for the LWR(RLWR) scheme, it can seem to be an equivalent modification to LWE(RLWE) scheme. Hence, we can use initial LWE(RLWE) scheme to make them into FHE. Since it looks almost the same, so we do not emphasis it particular.

## Chapter 4 Correctness

### 4.1 Correctness of LWR (RLWR) scheme

Lemma 5 (Correctness). Let $\mathbf{c}, \mathbf{A}, \mathbf{r}$ be described in the Encryption scheme of LWR (RLWR), then we can decrypt the message $m \in\{0,1\}$ using the secret key $\mathbf{s}$

Proof.

$$
\begin{aligned}
m & =\langle\mathbf{c}, \mathbf{s}\rangle_{q} \quad \bmod 2=\left\langle\mathbf{m}+\mathbf{A}^{T} \mathbf{r}, \mathbf{s}\right\rangle=\langle\mathbf{m}, \mathbf{s}\rangle+\left\langle\mathbf{A}^{T} \mathbf{r}, \mathbf{s}\right\rangle \\
& =m+2 \times 2 \times\left(\left\lceil\frac{1}{2} \mathbf{A}^{\prime} \mathbf{s}\right\rfloor-\frac{1}{2} \mathbf{A}^{\prime} \mathbf{s}\right) r \bmod 2 \\
& =m
\end{aligned}
$$

Lemma 6 (homomorphic properties). Let $c_{1}, c_{2}$ be two different messages encrypt by $\mathbf{s}$ and $\mathbf{A}, \mathbf{r}$ be described in Encryption scheme of LWR (RLWR), then we can decrypt the message $m_{1}+m_{2}$ and $m_{1} \times m_{2}$ using the secrete key $\mathbf{s}$

Proof.

$$
\begin{aligned}
m_{1}+m_{2} & =\left\langle\mathbf{c}_{1}+\mathbf{c}_{2}, \mathbf{s}\right\rangle_{q} \quad \bmod 2=\left\langle\mathbf{c}_{1}, \mathbf{s}\right\rangle_{q}+\left\langle\mathbf{c}_{2}, \mathbf{s}\right\rangle_{q} \bmod 2 \\
& =m_{1}+m_{2}
\end{aligned}
$$

$$
\begin{aligned}
m_{1} \times m_{2} & =\left\langle\mathbf{c}_{1} \otimes \mathbf{c}_{2}, \mathbf{s} \otimes \mathbf{s}\right\rangle=\left\langle\left(\mathbf{m}_{1}+\mathbf{A}^{T} r_{1}\right) \otimes\left(\mathbf{m}_{2}+\mathbf{A}^{T} r_{2}\right), \mathbf{s} \otimes \mathbf{s}\right\rangle \\
& =\left\langle\mathbf{m}_{1} \otimes \mathbf{m}_{2}, \mathbf{s} \otimes \mathbf{s}\right\rangle+\left\langle\mathbf{A}^{T} r_{1} \otimes \mathbf{m}_{2}, \mathbf{s} \otimes \mathbf{s}\right\rangle \\
& +\left\langle m_{1} \otimes \mathbf{A}^{T} r_{2}, \mathbf{s} \otimes \mathbf{s}\right\rangle+\left\langle\mathbf{A}^{T} r_{1} \otimes \mathbf{A}^{T} r_{2}, \mathbf{s} \otimes \mathbf{s}\right\rangle \\
& =m_{1} \times m_{2}
\end{aligned}
$$

### 4.2 Correctness of MPLWE(MPLWR) scheme

Lemma 7 (Correctness). Assume that $\alpha<1 /(16 \sqrt{\lambda t k})$ and $q \geq 16 t(k+1)$. With probability $\geq 1-d \cdot 2^{-\Omega(\lambda)}$ over the randomness of $(s k, p k) \leftarrow$ KeyGen, for all plaintext $\mu$ and with probability 1 over the randomness of Encrypt, we have $\operatorname{Decrypt}(s k, \operatorname{Encrypt}(p k, \mu))=$ $\mu$

## Proof. In [9] Lemma 4.1.

Lemma 8 (homomorphic properties). Let $c_{1}, c_{2}$ be two different messages encrypt by $\mathbf{s}$ and $\mathbf{A}, \mathbf{r}$ be described in Encryption scheme of MPLWE (MPLWR), then we can decrypt the message $m_{1}+m_{2}$ and $m_{1} \times m_{2}$ using the secrete key $\mathbf{s}$

Proof.

$$
\begin{aligned}
m_{1}+m_{2} & =\left\langle\mathbf{c}_{1}+\mathbf{c}_{2}, \mathbf{s}\right\rangle_{q}^{\odot_{d}}=\left\langle\mathbf{c}_{1}, \mathbf{s}\right\rangle_{q}^{\odot_{d}}+\left\langle\mathbf{c}_{2}, \mathbf{s}\right\rangle_{q}^{\odot_{d}} \\
& =m_{1}+m_{2}
\end{aligned}
$$

$$
\begin{aligned}
m_{1} \times m_{2} & =\left\langle\mathbf{c}_{1} \otimes \mathbf{c}_{2}, \mathbf{s} \otimes \mathbf{s}\right\rangle_{q}^{\odot_{d}}=\left\langle\left(\mathbf{m}_{1}+\mathbf{A}^{T} r_{1}\right)_{q}^{\odot_{d}} \otimes\left(\mathbf{m}_{2}+\mathbf{A}^{T} r_{2}\right), \mathbf{s} \otimes \mathbf{s}\right\rangle_{q}^{\odot_{d}} \\
& =\left\langle\mathbf{m}_{1} \otimes \mathbf{m}_{2}, \mathbf{s} \otimes \mathbf{s}\right\rangle_{q}^{\odot_{d}}+\left\langle\mathbf{A}^{T} r_{1} \otimes \mathbf{m}_{2}, \mathbf{s} \otimes \mathbf{s}\right\rangle_{q}^{\odot_{d}} \\
& +\left\langle m_{1} \otimes \mathbf{A}^{T} r_{2}, \mathbf{s} \otimes \mathbf{s}\right\rangle_{q}^{\odot_{d}}+\left\langle\mathbf{A}^{T} r_{1} \otimes \mathbf{A}^{T} r_{2}, \mathbf{s} \otimes \mathbf{s}\right\rangle_{q}^{\odot_{d}} \\
& =m_{1} \times m_{2}
\end{aligned}
$$

## Chapter 5 Optimization

### 5.1 Bootstrapping and Batching

A somewhat homomorphic encryption scheme is a scheme which contains addition and multiple properties at the same time (Roughly speaking). In [4], Gentry has proved that there exist an efficient transformation that given a description of a bootstrappable scheme $\varepsilon$ and a parameter $d=d(\lambda)$ outputs a description of another encryption scheme such that $\varepsilon^{(d)}$ is compact (which means the size of the ciphertext is bound) and $\varepsilon^{(d)}$ is homomorphic for all circuits of depth up to $d$.

Our scheme is obviously a somewhat homomorphic scheme, i.e. we still can make our scheme into bootstrappable. The advantage to make our scheme into bootstrapple is that in [5] it has described a way to batch the bootstrapping scheme and have high efficient in specific problems.

### 5.2 Public Key Compression for LWR

In [6], introduce a way to compress the public key size with a pseudo-random number generator $f$. However, it is not compatible to polynomial ring based encryption, hence we only introduce the public key compression for LWR.

- KeyGen $\left(1^{\lambda}\right)$ Generate a random prime integer $p$ of size $\eta$ bits. And randomly generate $\mathbf{a}_{i}$ 's, compute $b_{i}=4\left\lceil\frac{1}{2} a_{i} s\right\rfloor-a_{i} s$. Initialize a pseudo-random number generator $f$ with a random seed se. Use $f(s e)$ to generate a set of integers $\chi_{i} \in\left[0,2^{\gamma}\right)$ for $1 \leq i \leq \tau$. For all $1 \leq i \leq \tau$ compute:

$$
\delta_{i}=\left\langle\chi_{i}\right\rangle_{p}+\xi \cdot p-r_{i}
$$

where $r_{i} \leftarrow \mathbb{Z} \cap\left(-2^{\rho}, 2^{\rho}\right)$ and $\xi_{i} \leftarrow \mathbb{Z} \cap\left[0,2^{\lambda+\eta} / p\right]$. For all $1 \leq i \leq \tau$ compute:

$$
b_{i}=\chi_{i}-\delta_{i}
$$

Let $p k=\left(a_{0}, \ldots, a_{\tau}, s e, \delta_{0}, \ldots, \delta_{\tau}\right)$ and $s k=p$

We should store all of $a_{i}$ and about a one dimension terms $b_{i}$ 's, i.e. we store about $\tau \cdot \eta+\gamma+\eta$ bits public key instead of $2 \tau \eta$ public key, which is about halves the initial public key, but notice that we should calculate the public key each time we need to use.

## Chapter 6 Zero knowledge proof

In [3], it introduces a way to do Zero knowledge proof via fully homomorphic encryption. It is still compatible to our scheme. The generic protocol, between a prover P and a verifier V , is as follows.
$P_{1}$. Choose an encryption $c^{\prime}=b^{\prime}+r^{\prime}$ of zero and send $c^{\prime}$ to the verifier.
$V_{1}$. Select $e \leftarrow\{0,1\}$ and send $e$ to the prover.
$P_{2}$. If $e=0$, set $d=b^{\prime}$, or if $\mathrm{e}=1$, set $d=b+b^{\prime}$. Transmit $d$.
$V_{2}$. Verify that $d$ is a lattice point, and check that the noise $e c+c^{\prime}-d$ is well-formed and sufficiently small.

This is also our advantage to choose lattice based encryption. It is easily to compatible our scheme to other lattice based protocol.

## Chapter 7 Application

There are many situations we will likely to use fully homomorphic encryption. For example, machine learning may need a huge amounts of computing. However, it may be a hard time for a start-up company to buy high-performance computers. The solutions to this situation are to rend computing power via cloud computing. However, how to save data security? Hence, we can save our security via fully homomorphic encryption. Since we have addition properties and multiplication properties. We can do all kinds of computing in encrypted state.

Although we may spend more computing resource to keep the data security, it is still pay-off if the total spending time is fewer than using personal computer. Hence fully homomorphic encryption may be a good choice to keep data safe and save more times than usual.

Otherwise, fully homomorphic encryption may also be a good choice to do multiparty computation. Since we have addition properties and multiplication properties we can easily construct a scheme.

If you want to do secure multi-party computation, you can express the computation as a boolean circuit C, and you can easily transform any circuit so that it uses only AND gates and NOT gates. Then, it turns out that you can compute C on encrypted data, if the data was encrypted using a fully homomorphic encryption scheme, using the follow-
ing relationship: when working with 0,1 , AND can be done by multiplication (x AND y $=x y$ ), and NOT can be done with addition $(\operatorname{NOT}(x)=1-x)$. Since the fully homomorphic encryption lets you do addition, subtraction, and multiplication on encrypted values, it also lets you do NOT and AND on encrypted values, which is all you need to do secure multi-party computation.

## Chapter 8 Summary

We introduce a way to do FHE on LWR, MPLWE and MPLWR which have higher security than basic LWE scheme. And it seems that the variants of LWE have the similar properties. And we also introduce the public key compression, which is useful when the transfer costs may be high in certain case.

The table below show the different between these protocol. The estimate time showed in the table is calculated by the numbers of multiplication. We consider that we calculate 1 GB messages AES-128 with 3.60 GHz . And the multiplication in AES-128 contains 7568 multiplications over finite field. The finite field multiplication algorithm we use is the Montgomery modular multiplication with each piece 32 bits. The estimated performance times is about 484 times AES-128 computing times (about 14.48 (s) for 1GB AES-128).

|  | LWE | LWR | MPLWE | MPLWR |
| :---: | :---: | :---: | :---: | :---: |
| Public key | $\lceil(2 n+3) \log q\rfloor$ | $\lceil(2 n+3) \log q\rfloor$ | $(n+d+k-1) t$ | $(n+d+k-1) t$ |
| Secret key | $(n+1) \log q$ | $(n+1) \log q$ | $(n+d+k-1) \log q$ | $(n+d+k-1) \log q$ |
| Ciphertext | $(n+1) \log q$ | $(n+1) \log q$ | $(n+k) \log q+d \log q$ | $(n+k) \log q+d+k$ |
| Estimate time | $1.953(\mathrm{~h})$ | $1.954(\mathrm{~h})$ | $2.198(\mathrm{~h})$ | $2.210(\mathrm{~h})$ |
| Hardness | LWE $\leq$ LWR, LWE $\leq$ MPLWE $\leq$ MPLWR |  |  |  |

## Chapter 9 Future Work

In 2018, the most simple fully homomorphic encryption scheme DGHV is said to be broken via quantum computer in quantum polynomial times. For our scheme, we still do not have a security proof to withstand quantum computer. We hope that we can prove the quantum security to our scheme. Otherwise, it seems that we have to transmit more information on computing, if there is some transmitting error the message will be to break. We hope that we can solve this problem for example changing the scheme for code based encryption.

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